CLAIMS

Having thus described our invention, what we claim as new and desire to secure by Letters Patent is as follows:

1	1. A method for creating integrated security within electronic devices,
2	comprising the steps of:
3	concatenating one or more scan chains to create a storage element;
4	connecting the storage element to a comparator within an electronic
5	circuit wherein an output of the comparator enables a system component;
6	receiving a password from a user which becomes the system
7	security id code; and
8	configuring one or more said scan chains to customize the storage
9	element which represents said security id code by blowing integrated
10	electronic fuses.
1	2. A method according to claim 1, wherein the scan chains are composed
2	of latches or registers and are accessible externally via one or more serial
3	inputs or outputs.
1	3. A method according to claim 1, wherein the scan chains are sufficiently
2	long in order to represent passwords of variable lengths and to contain a
3	security id code of large magnitude.
1	4. A method according to claim 1, wherein the security id code is not
2	alterable once blown and cannot be read from the storage elements after
3	the security code is blown except by the comparator.
1	5. A method according to claim 1, wherein the electronic fuses are blown
2	if the current security code id is provided to enable the securing process to

3	occur.
1	6. A method according to claim 1, wherein the password is compared by
2	the comparator to contents of the storage element.
1	7. A method according to claim 1, wherein the password is validated for
2	size limits and character content.
1	8. A method according to claim 1, wherein the storage element is a
2	plurality of storage elements.
1	9. A method according to claim 1, wherein the comparator is a plurality of
2	comparators.
1	10. A method for creating integrated security within electronic devices,
2	comprising the steps of:
3	concatenating one or more scan chains to create a storage element
4	said storage element configured by integrated electronic fuses to represent
5	a system security id code;
6	connecting the memory element to a comparator within an
7	electronic circuit wherein the output of the comparator enables a system
8	component;
9	receiving a password from a user;
10	providing the password to the comparator;
11	comparing the password to the system security id code wherein the
12	comparator output enables a system component.
1	11. A method according to claim 10, wherein the scan chains are
2	composed of latches or registers and is accessible externally via one or
3	more serial inputs or outputs.

12. A method according to claim 10, wherein the scan chains are

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2	sufficiently long in order to represent passwords of variable lengths and to
3	contain a security id code of large magnitude.
1	13. A method according to claim 10, wherein the security id code is not
2	alterable and cannot be read from the storage elements except by the
3	comparator.
1	14. A method according to claim 10, wherein the password is compared by
2	the comparator to the contents of the storage element.
1	15. A method according to claim 10, wherein the password is validated for
2	size limits and character content.
1	16. A method according to claim 10, wherein the storage elements are a
2	plurality of storage elements.
1	17. A method according to claim 10, wherein the comparator is a plurality
2	of comparators.
1	18. An integrated security device for providing security within electronic
2	devices comprising:
3	a scan chain which is configured using electronic fuses to represent
4	a system security id code;
5	a comparator that compares a password entered by a system user to
6	the system security id code;
7	an output of the comparator which can enable a electronic
8	component or electronic device.

- 1 19. An integrated security device as recited in claim 18 wherein the scan
- 2 chain is a plurality of scan chains.
- 1 20. An integrated security device as recited in claim 18 wherein the
- 2 comparator is a plurality of comparators.